

# 2.5V Drive Nch+Nch MOSFET

# US6K1

#### Structure

Silicon N-channel MOSFET

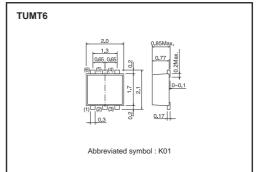
## ● Features

- 1) Low On-resistance.
- 2) Space savingsmall surface mount package (TUMT6).
- 3) Low voltage drive (2.5V drive).

## Applications

Switching

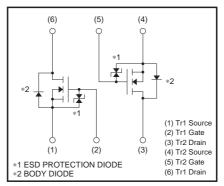
# ●Dimensions (Unit : mm)



# Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
US6K1		0

#### •Inner circuit



# ●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		$V_{DSS}$	30	V	
Gate-source voltage		V <sub>GSS</sub>	12	V	
Drain augrent	Continuous	I <sub>D</sub>	±1.5	А	
Drain current	Pulsed	IDP *1	±6	А	
Source current	Continuous	Is	0.6	А	
(Body diode)	Pulsed	I <sub>SP</sub> *1	6	А	
Total power dissipation		Pp *2	1.0	W / TOTAL	
		I D	0.7	W / ELEMENT	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

<sup>\*1</sup> Pw≤10µs, Duty cycle≤1% \*2 Mounted on a ceramic board

#### ●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	125	°C/W / TOTAL
Charmer to ambient	Kui(ch-a)	179	°C/W / ELEMENT

<sup>\*</sup> Mounted on a ceramic board

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	10	μΑ	V <sub>GS</sub> =12V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	30	-	_	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	IDSS	_	-	1	μΑ	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	0.5	-	1.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
		-	170	240	mΩ	I <sub>D</sub> = 1.5A, V <sub>GS</sub> = 4.5V
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	_	180	250	mΩ	I <sub>D</sub> = 1.5A, V <sub>GS</sub> = 4.0V
resistance		_	240	340	mΩ	I <sub>D</sub> = 1.5A, V <sub>GS</sub> = 2.5V
Forward transfer admittance	Y <sub>fs</sub> *	1.5	-	_	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1.5A
Input capacitance	Ciss	_	80	_	pF	V <sub>DS</sub> = 10V
Output capacitance	Coss	_	13	_	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	Crss	_	12	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	_	7	_	ns	V <sub>DD</sub> ≒ 15V
Rise time	tr *	_	9	_	ns	ID= 0.75A
Turn-off delay time	t <sub>d (off)</sub> *	_	15	_	ns	V <sub>GS</sub> = 4.5V R <sub>L</sub> = 20Ω
Fall time	t <sub>f</sub> *	_	6	_	ns	R <sub>G</sub> =10Ω
Total gate charge	Qg *	_	1.6	2.2	nC	V <sub>DD</sub> ≒15V
Gate-source charge	Qgs *	_	0.5	_	nC	Vgs= 4.5V
Gate-drain charge	Q <sub>gd</sub> *	_	0.3	_	nC	I <sub>D</sub> = 1.5A

<sup>\*</sup>Pulsed

# ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VsD	_	_	1.2	V	I <sub>S</sub> = 0.6A, V <sub>GS</sub> =0V

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#### •Electrical characteristics curves

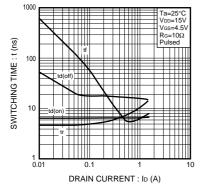


Fig.1 Switching Characteristics

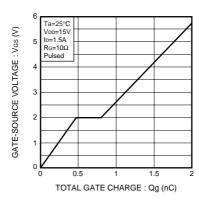


Fig.2 Dynamic Input Characteristics

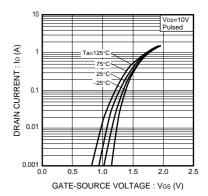


Fig.3 Typical Transfer Characteristics

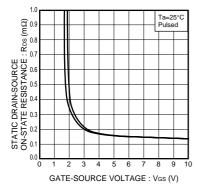


Fig.4 Static Drain-Source On-State Resistance vs. Gate source Voltage

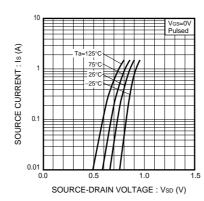


Fig.5 Source Current vs. Source-Drain Voltage

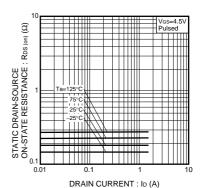


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current ( I )

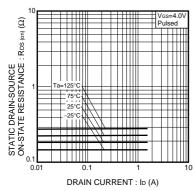


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current ( II )

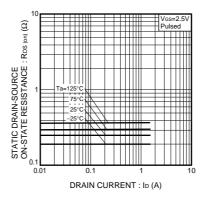


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current ( III )

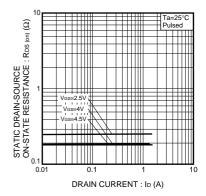


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current ( IV)

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